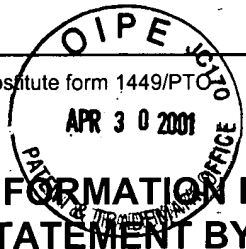


<div style="display: flex; align-items: center; justify-content: center;"> <div style="text-align: center;">  <p><b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b></p> </div> </div>				<b>Complete if Known</b>	
				Application Number	09/753,055
				Filing Date	December 28, 2000
				First Named Inventor	David William Boerstler
				Group Art Unit	<del>2019</del> 2634
Examiner Name	Unknown				
Sheet	1	of	1	Attorney Docket Number	AUS920000518US1

U.S. PATENT DOCUMENTS				
Examiner Initials	IDS Doc #	U.S. Patent Document Number	Name of Patentee or Applicant	Date of Publication MM-DD-YYYY
7	AA	5,436,939	Co, et al.	07/25/1995
7	AB	5,477,181	Li, et al.	12/19/1995
7	AC	4,669,024	Stacey	05/26/1987
7	AD	5,550,515	Liang, et al.	08/27/1996


  

FOREIGN PATENT DOCUMENTS						
Examiner Initials	IDS Doc #	Office	Foreign Patent Document Number	Name of Patentee or Applicant	Date of Publication	English Translation

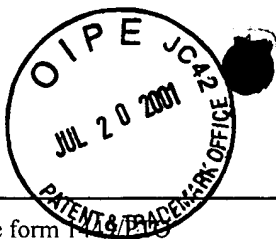
  

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	IDS Doc #	Include the name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	English Translation
7	AE	"Dynamic Behaviour of a Phase Locked Loop Using D-Type Phase Detector and Non-Linear Voltage-controlled Oscillator" by David W. Boerstler, Data Systems Division, Kingston, NY 12401, 91A001478, TR-21.1428	Yes
7	AF	"A Self-Correcting Clock Recovery Circuit" by Charles R. Hogge, Jr., IEEE Journal of Lightwave Technology 1983+ UE, IEEE Journal of Lightwave Technology Issue Vol. LT-3, Dec. 1985, Pages 1312-1314, 05W17, HDR 0020316	Yes
7	AG	"A 0.5 UM CMOS 4.0 Gbit/s Serial Link Transceiver with Data Recovery Using Oversampling" by Chih-Kong Ken Yang, Ramin Farjad-Rad, and Mark A. Horowitz, IEEE JSSC Issue Vol. 33, No. 5, May 1998, 07T20, HDR 0020243	Yes
7	AH	"Single-Chip 1062 Mbaud CMOS Transceiver for Serial Data Communication" by John F. Ewen, Albert X. Widmer, Mehmet Soyuer, Kevin R. Wrenner, Ben Parker, Herschel A. Ainspan, ISSCC Digest Issue Vol. 38, Feb. 1995, Pages 32-33, 07T20, HDR 0020244	Yes

Examiner Signature		Date Considered	6/27/04
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



<b>Substitute form 1</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<b>Complete if Known</b>		
				Application Number	09/753,055	
				Filing Date	December 28, 2000	
				First Named Inventor	DAVID WILLIAM BOERSTLER	
				Group Art Unit	<del>2019</del> 2634	
				Examiner Name	UNKNOWN	
Sheet	1	of	1	Attorney Docket Number	AUS920000518US1	
<b>U.S. PATENT DOCUMENTS</b>						
Examiner Initials	IDS Doc #	U.S. Patent Document Number	Name of Patentee or Applicant		Publication Date MM-DD-YYYY	
7	AA	Attorney Docket No. AUS920000519US1; App. No. 09/753,054	David William Boerstler		Application Filed: December 28, 2000	
<b>FOREIGN PATENT DOCUMENTS</b>						
Examiner Initials	IDS Doc #	Office	Foreign Patent Document No.	Name of Patentee or Applicant	Date of Publication	English Translation
<b>OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS</b>						
Examiner Initials	IDS Doc #	Include the name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published				English Translation
Examiner Signature				Date Considered	6/27/04	

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